V3S - VHDL and SystemVerilog for Visual Studio 2010-2019 v2.1.0

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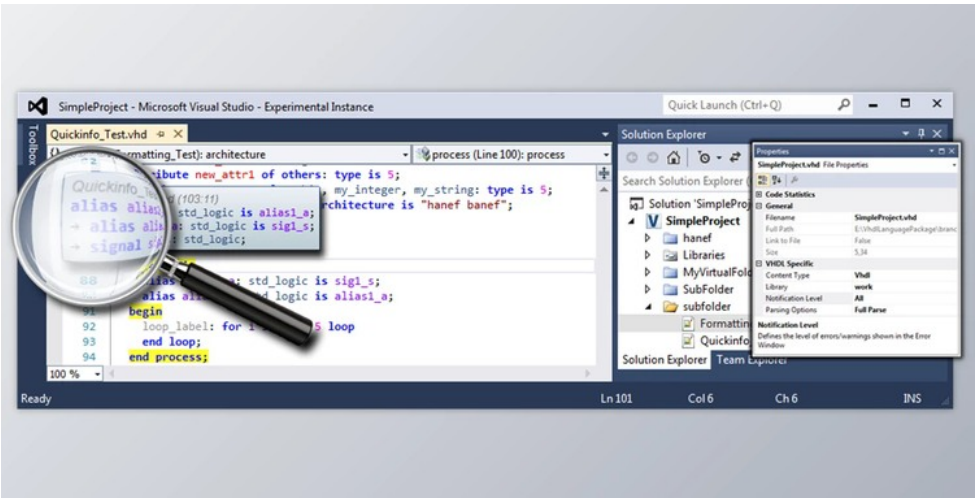

VHDL, Verilog, SystemVerilog for Visual Studio. Complex IDE support for VHDL and Verilog/SystemVerilog; Supports semantic/syntax highlighting, renaming, quick info, code snippets, IntelliSense, code completion, etc...

V3S completely supports VHDL2008, and I am always working to enhance Verilog/SystemVerilog service too. Download the expansion - it'll work without constraints for a 30-day trial interval.

VBiS is an extension for Microsoft's most outstanding Visual Studio. It enables developers to use Visual Studio as a code editor for VHDL (and Verilog/SystemVerilog) jobs. Adhering to the abundant feature set of this C# programming language, V3S provides many helpful functions for efficiently programming in VHDL (or even Verilog/SystemVerilog).

Sort less, gain more, refactorize
V3S closely integrates into Visual Studio and provides all features that one expects out of a cutting-edge Integrated Development Environment. Refactorizatoin, code completion/autocomplete, intelligent indentation, and protracted snippets (templates) are just some examples that will bring your programming performance to another level.

Historical error detection
Syntactic- and - Incorrect error checks are done as you're typing, thus enabling you to fix syntactic and many semantic mistakes on the fly without integrating your own synthesis tool. You do not need to switch between programs, therefore saving substantial time.

Successful navigation
Big HDL jobs are much less ordered and well-arranged as with object-oriented programming languages. V3S provides many features for fast navigating the code-base. Visit a symbol's definition with only 1 keystroke, use the convenient navigation bar to jump into various layout modules, or locate all references to get a fast summary of logo usage.

Quick info is the buddy.
Large projects generally have many distinct kinds, signs, and constants. Looking up types or some continuous values will have an immense quantity of your valuable time. V3S supplies Quickinfo - hover the cursor over a logo. You will find a pop-up containing type definitions and default values, automated number conversion, and straightforward expression evaluation.

Import existing jobs
if you already have existing HDL jobs, e.g., for your Altera Quartus or the Xilinx ISE/Vivado Software Suite, then it is possible to use the New Project Wizard to import your project into Visual Studio. To get a fast start, I offer a Hello World plus a Sample Project, each of which may be made with just a couple of clicks.

V3S - VHDL and SystemVerilog for Visual Studio Great Features:

- Refactorization, Renaming
- Library View
- Hierarchy View
- Parameter Help
- Produce Testbench Wizard
- Assess habit naming rules/conventions
- Quicksearch conversation
- Discover all references
- Goto Declaration/Definition
- Import existing Altera/Xilinx jobs

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标签

Components .Net Other

- Type-time mistake checking
- Smart indentation, smart copy/paste
- Record resolution
- Code conclusion, autocomplete
- Comment/Uncomment code cubes
- Block choice
- Outlining/Code folding
- Code Statistics
- Safe Form Support
- Extended code snippets (fast component instantiation,...)
- Tuple highlighting (braces, if-then-else-else-end, case-when-end, ...)
- Navigation (goto definition/declaration of elements, factors, signals,...)
- Syntax/semantic highlighting (forms, signs, interfaces, libraries, constants, attributes,...)
- Quick info/Mouse hover (amount conversion bin/hex/dec, kind resolution, sign type info,...)

版本列表

V3S - VHDL Verilog SystemVerilog for VS v2.1.0 + License Key

V3S - VHDL Verilog SystemVerilog for VS v2.0.2 + License Key

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